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Docket No.: 1999 P 1778

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

By: 

Date: December 24, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Kux et al.  
Applic. No. : 10/002,925  
Filed : November 2, 2001  
Title : Method and Device for Securing a Multi-Dimensionally Constructed Chip Stack and Chip Configuration  
Attention : Office of Petitions

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PETITION UNDER 37 C.F.R. 1.10(c)

Hon. Commissioner of Patents and Trademarks,  
Washington, D. C. 20231

Sir:

The facts leading to this petition are as follows:

The above-identified application was delivered by the "Express Mail Post Office to Addressee" service of the US PS on November 2, 2001. Therefore, the filing date accorded to this application by the Office should be November 2, 2001. However, the postcard which accompanied the application was returned by the PTO showing an incorrect filing date of November 4, 2001.

Enclosed herewith, in accordance with Rule 1.10 (c), is:

- a copy of the cover sheet accompanying the application papers with a mailing certificate under 37 C.F.R. 1.10 properly executed on November 2, 2001
- a copy of the Express Mail Label with a Post Office stamp of November 2, 2001
- a copy of the postcard
- a declaration by the mail clerk

In view of the foregoing, applicants request that the filing date be corrected to show November 2, 2001.

I hereby declare that the foregoing facts have been stated on a personal knowledge basis and that the above-noted paper was submitted timely in accordance with the rules and regulations. I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

  
For Applicants

Date: December 24, 2001

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